

Japan Traceability Committee Meeting Summary and Minutes

Japan Standards Fall Meetings 2013
October 4, 2013, 11:00-12:00
JPR Bldg, Conference Room1, SEMI Japan Office, Tokyo, Japan

Next Committee Meeting

December 6, 2013, 15:00-1700, Japan Standard Time
SEMICON Japan 2013, Makuhari Messe, Chiba, Japan

Committee Announcements (optional)

None

Table 1 Meeting Attendees

Co-Chairs: Yoichi Iga (Renesas Electronics), Hirokazu Tsunobuchi (Keyence)

SEMI Staff: Hirofumi Kanno

<i>Company</i>	<i>Last</i>	<i>First</i>	<i>Company</i>	<i>Last</i>	<i>First</i>
JIPDEC	Aoki	Takashi	Renesas Electronics	Iga	Yoichi
Fujitsu Semiconductor	Hirasue	Terumi	Fujitsu Semiconductor	Iida	Kiyokazu
Mitsubishi Electric	Ishihara	Takashi	NHK Spring	Shimizu	Yasufumi
Keyence	Tsunobuchi	Hirokazu	Denso Wave	Takai	Hiromitsu
The University of Electro-Communications	Sato	Akashi	AIST	Sakane	Hirofumi
Hitachi Kokusai Electric	Matsuda	Mitsuhiro			

Table 2 Leadership Changes

<i>Group</i>	<i>Previous Leader</i>	<i>New Leader</i>
Fiducial Mark Interoperability Task Force	--	Hirokazu Tsunobuchi (Keyence)

Table 3 Ballot Results (or move to Section 4, Ballot Review)

None

Documents reviewed by the committee outside of the balloting process:

Doc.5652, *Addition of New Related Information: Example of Track and Traceability Supply Chain System to SEMI T21 Specification for Authentication of Semiconductors and Related Products*

- Passed committee review.

Note: the Document will be submitted for the GCS approval

Table 4 Authorized Ballots (or move to Section 7, New Business)

None

Table 5 Authorized Activities (or move to Section 7, New Business)

#	Type	SC/TF/WG	Details
--	TFOF	Fiducial Mark Interoperability Task Force	TF Charter: Standardization of fiducial mark as the alternative to the notch is now under development at the International 450mm Wafer Task Force under the Global Silicon Wafer Committee.

Table 5 Authorized Activities (or move to Section 7, New Business)

#	Type	SC/TF/WG	Details
			<p>Adoption of fiducial mark will affect not only Si wafer specification and related Standards but also Standards owned by other Global Technical Committees that reference the Notch. Furthermore, as readability of fiducial mark may change due to intentional and unintentional interaction of wafer surface and various tools in their process apparatuses and with wafer handling mechanisms, standardization of fiducial mark detection data and their communication is deemed necessary to support traceability and to compensate deteriorated readability of Fiducial Mark.</p> <p>In order to address above cross disciplinary challenge, this Task Force is established under five Global Technical Committees (Silicon Wafer TC, PI&C TC, I&C TC, Assembly & Packaging TC and Traceability TC), and develops the Standards to support the fiducial mark.</p>
5652	SNARF	Japan PV Traceability Task Force	Addition of New Related Information: Example of Track and Traceability Supply Chain System to SEMI T21 Specification for Authentication of Semiconductors and Related Products

Note: SNARFs and TFOFs are available for review on the SEMI Web site at: <http://downloads.semi.org/web/wstdsbal.nsf/TFOFSNARF>

Table 6 New Action Items (or move to Section 8, Action Item Review)

None

Table 7 Previous Meeting Actions Items (or move to Section 8, Action item Review)

None

1 Welcome, Reminders, and Introductions

Yoichi Iga called the meeting to order at 11:00. The meeting reminders on antitrust issues, intellectual property issues and holding meetings with international attendance were reviewed. Attendees introduced themselves.

2 Review of Previous Meeting Minutes

The committee reviewed the minutes of the previous meeting.

Motion: Approve the minutes of the previous meeting
By / 2nd: Hirokazu Tsunobuchi (Keyence), Takashi Aoki (JIPDEC)
Discussion: None
Vote: 9:0

3 Liaison Reports

3.1 North America Traceability Committee

No update

3.2 SEMI Staff Report

Hirofumi Kanno (SEMI) gave the SEMI Staff Report. Of note:

- Global SEMI Events

- Global Standards Meetings Schedule
- Ballot Critical Dates
- Publication Update
- ISC A&R SC Summary May and June 2013
- Preparation for SEMICON Japan 2013
- Contact Information

Attachment: 02, SEMI Staff Report 2013 July R0.2a

4 Ballot Review

4.1 None

5 Subcommittee & Task Force Reports

5.1 5 Years Review Task Force

Hirokazu Tsunobuchi (Keyence) reported there was no update for the Task Force.

5.2 Japan PV Traceability Task Force

Yoichi Iga (Renesas Electronics) reported for the Task Force.

6 Old Business

None

7 New Business

7.1 SNARF Proposal for Addition of New Related Information: Example of Track and Traceability Supply Chain System to SEMI T21 Specification for Authentication of Semiconductors and Related Products

Takashi Aoki (JIPDEC) submitted the SNARF for Addition of New Related Information: Example of Track and Traceability Supply Chain System to SEMI T21 Specification for Authentication of Semiconductors and Related Products.

Motion: Approve the SNARF (Doc.5652) for Addition of New Related Information: Example of Track and Traceability Supply Chain System to SEMI T21 Specification for Authentication of Semiconductors and Related Products

By / 2nd: Takashi Aoki (JIPDEC), Hirokazu Tsunobuchi (Keyence)

Discussion: None

Vote: 8:0

As a Document reviewed by the committee outside of the balloting process:

Motion: Approve Doc. 5652, the addition of New Related Information: Example of Track and Traceability Supply Chain System to SEMI T21 Specification for Authentication of Semiconductors and Related Products

By / 2nd: Takashi Aoki (JIPDEC), Hirokazu Tsunobuchi (Keyence)

Discussion: None

Vote: 8:0

Attachment: 01,SNARFform 130910

Attachment: 02,Procedual Voting Sheet for Addition of RI to T21

7.2 TFOF Proposal for Fiducial Mark Interoperability Task Force

Mitsuhiro Matsuda reported the new Task Force activity proposal.

Motion: Approve the TFOF for Fiducial Mark Interoperability Task Force

By / 2nd: Mitsuhiro Matsuda (Hitachi Kokusai Electric), Hirokazu Tsunobuchi (Keyence)

Discussion: None

Vote: 8:0

Motion: Approve the appointment of Hirokazu Tsunobuchi as a co-leader of the Feducial Mark Interoperability Task Force

By / 2nd: Mitsuhiro Matsuda (Hitachi Kokusai Electric), Hirokazu Tsunobuchi (Keyence)

Discussion: None

Vote: 8:0

Attachment: 03, 20130829FidutialMarkIOTF_R0.2

8 Action Item Review

8.1 *Open Action Items*

None

8.2 *New Action Items*

None

9 Next Meeting and Adjournment

The next meeting of the Japan Traceability committee is scheduled for Friday, December 6 at SEMICO Japan 2013 in Makuhari Messe, Chiba, Japan.

Respectfully submitted by:
Hirofumi Kanno
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SEMI Japan
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Email: hkanno@semi.org

Minutes approved by:

Yoichi Iga (Renesas Electronics)	<Date approved>
Hirokazu Tsunobuchi (Keyence)	<Date approved>

Table 8 Index of Available Attachments #1

#	<i>Title</i>	#	<i>Title</i>
01	SNARFform 130910		
02	Procedural Voting Sheet for Addition of RI to T21		
03	20130829FidutialMarkIOTF_R0.2		

#1 Due to file size and delivery issues, attachments must be downloaded separately. A .zip file containing all attachments for these minutes is available at www.semi.org. For additional information or to obtain individual attachments, please contact [SEMI Staff Name] at the contact information above.