## Record of Letter Ballot Review by TC Chapter for Procedural Review

Region/Locale: North America Global Technical Committee: 3DS-IC TC Chapter Cochairs: Richard Allen/NIST, Chris Moore/Frontier Semiconductor, Sesh Ramaswami/Applied Materials Standards Staff: Laura Nguyen

	Scheduled in Background Statement	Actual
Date	07/12/2017	07/12/2017
Location	San Francisco, CA	San Francisco, CA
Reason for Change of Date and/or Location (if changed)		

Note: See *Regulations* ¶ 9.5 Exception for allowable reason to change.

#### I. Document Number and Title

<b>Document Number</b>	Document Title
5822A	New Standard, Specification for Reference Material
	for Bonded Wafer Stack Void Metrology

## II. Tally

Standards staff to fill in.

Voting Tally: As-cast tally after close of voting period

Note: A minimum of 60% of the Voting Interests that have TC Members within the global technical committee that issued the Letter Ballot must return Votes. (*Regulations* ¶ 9.7.1.1)

#### Voting Tally (with example values):

Voting Interest:	<b>Returned Votes</b>		Distribution		Return Rate	
Letter Ballot	41	÷	62	=	66.1%	≥60%
Intercommittee Ballot	34					
Voting Interest Reject(s)	0		Total	Vote	rs with Rejects	0
Voting Interest Accept(s)	35					

Note: See Regulations § 3.2.1 for definition of Voting Interest.

**III. Rejects** None

## **IV. Other Technical Issues** None

# **V. Comments** V- (i) Voters' Comments

Commenter 1 (Mashiro Tsuriya/iNEMI) - Comment 1

Comment	Only	1. Void size (nominal+tolerance): Only 100um void size is found in the document which is 0.1um. How about other void size tolerance? If no information, is there any way to prepare the Void metrology study sample?								
	The	TC Chapter agreed to do one of the following actions.								
	*No	motion is required in this step.								
		Already addressed by Commenter #, Comment #								
	X	No further action was taken by the TC Chapter.								
Action	x	Refer to the TF for more consideration. TF Response: His comment is correct, in that we only set a tolerance for a single size, which is the reference size of 100 um. The user is then given the option of choosing the tolerances for different size voids. TF agrees that the document needs no changes based on this comment.								
		New Business								
		Editorial Change								

#### Commenter 1 (Mashiro Tsuriya/iNEMI) - Comment 2

Comment	2. Fig4 Location ID: ID coding is very confusing. It is better to assign the simple address for each void. (example: X from right to left : 1, 2, 3, Y axis: from top to bottom: A, B, C,)								
	The	The TC Chapter agreed to do one of the following actions.							
	*No	motion is	requ	ired in this step.					
Δ		Already ad	ddres	sed by Commenter #, Comment #					
Action		No further	r action was taken by the TC Chapter.						
٢		Refer to the TF for more consideration.							
		New Business							
	X	Editorial C	hang	je					
		Options		Case 1: No vote in this section:					
		for editorial		To be included and voted on as a group in § VI. <i>Editorial Changes Other than Those Voted on in § V</i> .					
		change (check		Case 2: Voted in this section:					
		one)	X	Original section number and at least one full sentence are required in "FROM" and "TO" fields.					

Π		SEMI 3D13 -	ction/Paragraph 4/4.1 - Guide for Measuring Voids in Bonded Wafer Stacks 32-0715 – Round Robin Study of Method for Measurement of Voids in Bonded Pairs of s			
ditor		TO: Section/Paragraph 4/4.1				
ial C	1	SEMI 3D13 – Guide for Measuring Voids in Bonded Wafer Stacks				
Editorial Changes		SEMI AUX032-0715 – Round Robin Study of Method for Measurement of Voids in Bonded Pairs of Silicon Wafers				
SG		SEMI M20 – Practice for Establishing a Wafer Coordinate System				
		ID coding is	<b>n (If necessary)</b> consistent with the approach in SEMI M20, Figure 3a e need SEMI M20 in list of referred standards.			
М	otion		To approve above editorial change(s)			
М	otion	n by/2 <sup>nd</sup> by	Steve Martell (Sonoscan) / Bevan Wu (BW & Associates/ITRI)			
Di	iscus	sion	None			
Vo	ote		8 Y 0 N; Motion passed.			

# V-(ii) Comments Created by Handling Negative None

## VI. Editorial Changes Other than Those Voted on in § V None

# **VII. Approval Conditions Check**

#### VII. - (i). Approval Rate

APPROVAL CONDITION 1: All Negatives have been discussed and were withdrawn, found not related, found not persuasive, or addressed by a technical change. (*Regulations* ¶ 9.7.1.2)

APPROVAL CONDITION 2: At least 90% of the sum of valid Voting Interest Accept and Voting Interest Reject Votes must be Accept. (*Regulations* ¶ 9.7.1.3)

Note: If both approval conditions are not satisfied, the Document fails.

		Accepts		(Accepts + Valid Rejects)			
Approval Rate	=	35	/	35	=	100.0%	≥90%

#### VII. – (ii) Approval Level (check one)

Note: See Regulations § 9.7.2 for further information.

#### Globally Approved (No Ratification Ballot needed):

The Letter Ballot meets the Letter Ballot approval conditions for the global technical committee.

#### **Need a Ratification Ballot:**

The Letter Ballot meets the Letter Ballot approval conditions for the TC Chapter and a Ratification Ballot will be issued to validate technical changes.

## **VIII. Safety Check**

Χ

#### Note: See Regulations § 15 for further information.

	x	This is not a Safety Document, when all safety-related information is removed, the Document is still technically sound and complete. ( <i>Regulations</i> $\P$ 8.7.1)							
Motior			This is a Safety Document, when all safety-related information is removed, the Document is not technically sound and complete. ( <i>Regulations</i> ¶ 8.7.2)						
ſ		Safety Checklist ( <i>Regulations</i> ¶ 15.3) is complete and has been included with the Document throughout the balloting process. ( <i>Regulations</i> ¶ 15.1.2)							
	Noti	ion b	oy/2 <sup>nd</sup> by	Steve Martell (Sonoscan) / Bevan Wu (BW & Associates/ITRI)					
Discussion			ission	None					
	Vote			8 Y 0 N; Motion passed.					

# IX. Intellectual Property (IP) Check

Note: This Letter Ballot may cover all or part of a Standard or Safety Guideline. This IP check applies to the entire Standard or Safety Guideline. See *Regulations* § 16 for further information.

x	The TC Chapter meeting chair asked those participating, if they were aware of any potentially material patented technology or copyrighted items <sup>*</sup> in the Standard or Guideline. ( <i>Regulations</i> $\P$ 8.8.1)				
	X No potentially material patented technology or reproduction of copyrighted items is known.		GO TO SECTION X.		

\* Note: Such potentially material patented technology or copyrighted items might have become known since the Standard or Safety Guideline was last reviewed, or might become relevant due to this Letter Ballot.

# X. Action for This Document

Motion	x	This Docu ISC A&R S	This Document passed TC Chapter review with editorial changes and will be forwarded to the ISC A&R SC for procedural review.						
		on by/ <sup>d</sup> by	Steve Martell (Sonoscan) / Bevan Wu (BW & Associates/ITRI)						
6	Disc	ussion	None						
	V	ote	7 Y 0 N						
F	Final Action		X Motion passed						
	mai		Motion failed						

#### Standards staff to record the result of the A&R procedural review here:

		Approved for publication
A&R		Approved pending acceptance of the Ratification Ballot
Aan		Not approved
	Re	eason: